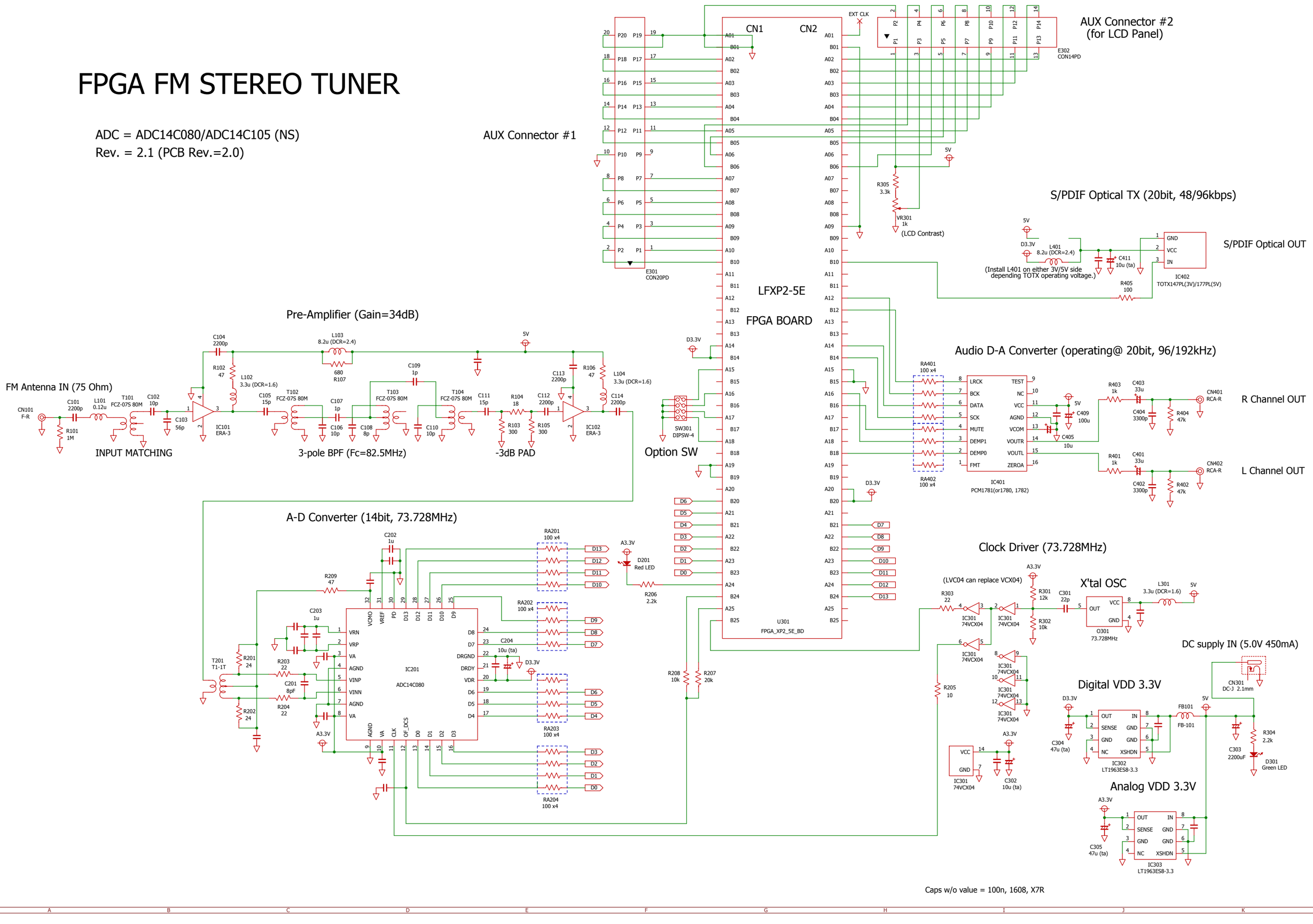
FPGA FM STEREO TUNER

ADC = ADC14C080/ADC14C105 (NS)
Rev. = 2.1 (PCB Rev.=2.0)



Caps w/o value = 100n, 1608, X7R